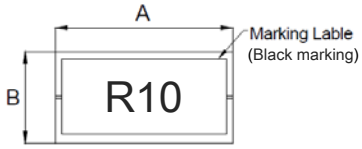
Trans-Inductor Voltage Regulator

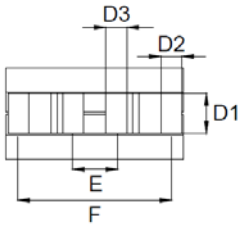
TLVR120611-Series-S0A

1. Dimension

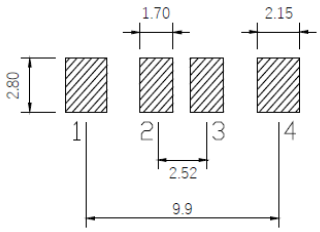
Top view



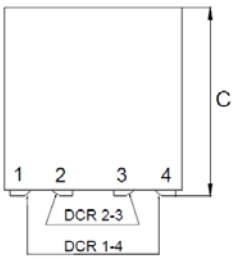
Bottom view



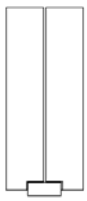
Recommend PC Board Pattern



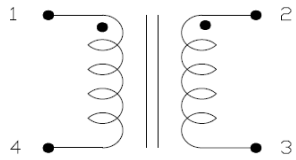
Front view



Side view



Schematic Diagram



Series	A(mm)	B(mm)	C(mm)	D1mm)	D2(mm)	D3(mm)	E(mm)	F(mm)
TLVR120611	12.0 max	6.0 max	11.05 max	2.30 typ	1.65 typ	1.42 typ	2.52 typ	9.90 typ

* PAD surface flatness 0.1mm max.
* Recommended: modules should be surface- mounted on the second time(last time) of customer's double-sided PCB to prevent shift of parts.
* Before soldering, be sure to preheat components. The recommended preheating condition is 150°C for 3 minutes.

2. Part Numbering

TLVR	120611	-	R10	K	-	S0A
A	B		C	D		E

A: Series
B: Dimension
C: Inductance R10=100nH
D: Inductance Tolerance K=±10%
E: Inner Code

3. Specification

TAI-TECH Part Number	L (nH) 1-4/2-3 ±10%	L2 (nH)@Isat1 Min 1-4	DCR (mΩ) ±10%		Isat 1 (A) Note b	Isat 2 (A) Note b	Isat 3 (A) Note b	Irms (A) 1-4 Note a	Marking Code
			1-4	2-3	@25℃	@100℃	@125℃		
TLVR120611-R07K-S0A	70	50	0.125	0.60	180	156	143	75	R07
TLVR120611-R08K-S0A	80	57	0.125	0.60	157	137	125	75	R08
TLVR120611-R10K-S0A	100	72	0.125	0.60	126	109	100	75	R10
TLVR120611-R11K-S0A	110	79	0.125	0.60	114	99	91	75	R11
TLVR120611-R12K-S0A	120	86	0.125	0.60	105	91	84	75	R12
TLVR120611-R15K-S0A	150	108	0.125	0.60	84	73	67	75	R15
TLVR120611-R17K-S0A	170	122	0.125	0.60	74	64	59	75	R17
TLVR120611-R20K-S0A	200	144	0.125	0.60	62	53	48	75	R20

Note:

1. L@ 100kHz, 0.1Vrms, 0A, 25℃.
2. L2 @ 100kHz, 0.1Vrms, I_{SAT}.
3. DCR @ 25℃, test DCR1-4 & DCR2-3 which was shown on dimension page.
4. Operating Temperature: -40°C~ +125°C(Including self-temperature rise).

a. I_{RMS}: Irms is the DC current which causes the surface temperature of the part increase approximately 40℃.
b. $I_{SAT}1$: Peak current for approximately 20% rolloff at +25℃
b. $I_{SAT}2$: Peak current for approximately 20% rolloff at +100℃
b. $I_{SAT}3$: Peak current for approximately 20% rolloff at +125℃