

1. Features

1. Low loss realized with low DCR.
2. High performance realized by metal dust core.
3. Ultra low buzz noise, due to composite construction.
4. 100% Lead(Pb)-Free and RoHS compliant.
5. High reliability -Reliability test complied to AEC-Q200.



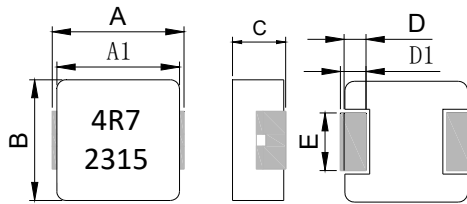
AEC-Q200



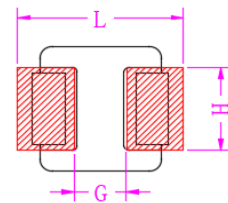
2. Applications

Automotive applications.

3. Dimensions



Recommend PC Board Pattern



Series	A(mm)	A1(mm)	B(mm)	C(mm)	D(mm)	D1(mm)	E(mm)
TMPV0504SPV	5.5±0.3	4.9±0.3	5.25±0.2	3.8±0.2	1.1±0.3	1.3±0.3	3.0±0.3

L(mm)	G(mm)	H(mm)
6.7	2.1	3.5

Note: 1.PCB layout is referred to standard IPC-7351B
 2. The above PCB layout reference only.
 3. Recommend solder paste thickness at 0.15mm and above.

4. Part Numbering

TMPV
0504
SPV
-
4R7
M
-
N
-
DFC

A B C D E F G

A: Series
 B: Dimension
 C: Type
 D: Inductance
 E: Inductance Tolerance
 F, G: Code

BxC
 Standard.
 4R7=4.70uH
 K=±10%, L=±15%, M=±20%, N=±25%, Y=±30%
 Marking direction cannot decide polarity.
 Marking: Black.4R7and 2315 (23:YY,15:WW, follow production date).

5. Specification

Part Number	Inductance L0 A(μH) ±20%	I _{rms} (A)		I _{sat} (A)		DCR (mΩ) Typ	DCR (mΩ) Max
		Typ	Max	Typ	Max		
TMPV0504SPV-R47MN-DFC	0.47	13.5	12.5	16.3	14.8	5.5	6.3
TMPV0504SPV-R68MN-DFC	0.68	12.5	11.5	15.2	13.7	7.5	8.6
TMPV0504SPV-1R0MN-DFC	1.0	11.5	10.5	13.0	11.5	9.8	11.3
TMPV0504SPV-1R5MN-DFC	1.5	9.0	8.0	12.0	10.5	14.7	16.2
TMPV0504SPV-2R2MN-DFC	2.2	7.8	7.0	11.0	9.5	21.8	24.0
TMPV0504SPV-3R3MN-DFC	3.3	6.8	6.2	9.0	8.0	30.0	34.5
TMPV0504SPV-4R7MN-DFC	4.7	6.1	5.7	8.1	7.2	35.6	39.2
TMPV0504SPV-6R8MN-DFC	6.8	5.2	4.7	6.3	5.4	50.0	57.5
TMPV0504SPV-100MN-DFC	10.0	4.0	3.6	5.2	4.6	82.0	92.0
TMPV0504SPV-150MN-DFC	15.0	3.0	2.8	3.8	3.2	100.0	120.0
TMPV0504SPV-220MN-DFC	22.0	2.7	2.4	3.5	3.0	170.0	187.0

Note:

1. Test frequency : Ls : 100KHz /1.0V.
2. All test data referenced to 25℃ ambient.
3. Testing Instrument(or equ) : Agilent 4284A,E4991A,4339B,KEYSIGHT E4980A/AL,chroma3302,3250,16502.
4. Heat Rated Current (I_{rms}) will cause the coil temperature rise approximately ΔT of 40℃
5. Saturation Current (I_{sat}) will cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp rise) should not exceed 165℃under worst case operating conditions.Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. I_{rms} Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.
Therefore temperature rise should be verified in application conditions.
8. Rated DC current: The lower value of I_{rms} and I_{sat}.

6. Typical Performance Curves

