

1. Features

1. Low loss with low DCR.
2. High performance realized by metal dust core.
3. Ultralow buzz noise, due to composite construction.
4. 100% Lead(Pb)-Free & Halogen-Free and RoHS compliant.
5. High reliability-complied with AEC-Q200.

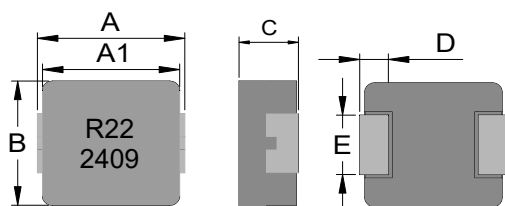
AEC-Q200



2. Applications

For automotive applications.

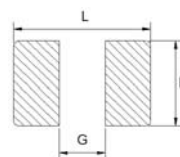
3. Dimensions



A	A1	B	C	D	E
8.8±0.4	8.1±0.2	8.4±0.3	3.8±0.2	1.6±0.3	5.0±0.3

Unit:mm

Recommend PC Board Pattern



L	G	H
9.6	4.5	5.5

Note:

1. PCB layout is referred to IPC-7351B standard.
2. The above PCB layout is for reference only.

4. Part Numbering

FTMP	8040	HPV	-	R22	MG	-	D
A	B	C		D	E		F

A: Series

B: Dimension

C: Type

D: Inductance

E: Inductance Tolerance

F: Code

BxC

HP:H:Carbonyl Powder,P:PAD broaden.V:vehicle.

R22=0.22uH

M=±20%

Marking: Black.R22 and 2409 (24 YY,09 WW,follow production date).

Anti-static packaging

5. Specification

Part Number	Inductance (uH) $\pm 20\%$ @ 0 A DC	Irms (A)	Isat (A)	DCR (mΩ)	
		Typ	Typ	Typ	Max
FTMP8040HPV-R22MG-D	0.22	31	60	1.60	1.76
FTMP8040HPV-R33MG-D	0.33	30	55	2.0	2.20
FTMP8040HPV-R47MG-D	0.47	28	40	2.6	2.86
FTMP8040HPV-R56MG-D	0.56	25	38	2.7	2.97
FTMP8040HPV-R68MG-D	0.68	23	36	3.1	3.41
FTMP8040HPV-R82MG-D	0.82	21	32	3.7	4.10
FTMP8040HPV-1R0MG-D	1.0	18	29	4.5	4.95
FTMP8040HPV-1R5MG-D	1.5	17	27	6.6	7.30
FTMP8040HPV-2R2MG-D	2.2	16	25	10.8	11.9
FTMP8040HPV-3R3MG-D	3.3	14	22	15	16.5
FTMP8040HPV-4R7MG-D	4.7	8.5	19	26.8	29.5
FTMP8040HPV-5R6MG-D	5.6	7.5	17	30	35
FTMP8040HPV-6R8MG-D	6.8	6.5	16.5	40	46
FTMP8040HPV-8R2MG-D	8.2	6.0	16	44	51
FTMP8040HPV-100MG-D	10.0	5.6	10	53	61

Note:

1. Test frequency : L_s : 100KHz / 1.0V.
2. All test data referenced to 25°C ambient.
3. Testing Instrument (or eq.): Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.
4. Heat rated current (Irms): current that cause temperature to rise approximately ΔT of 40°C.
5. Saturation current (Isat): current that cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp. rise) should not exceed 125°C under worst case operating conditions. Circuit design, PCB trace and thickness, airflow and other cooling conditions all affect the part temperature. Part temperature should be verified in the end application.
7. Irms Test : temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.
8. Rated DC current: the lower value of Irms and Isat.

6. Typical Performance Curves

